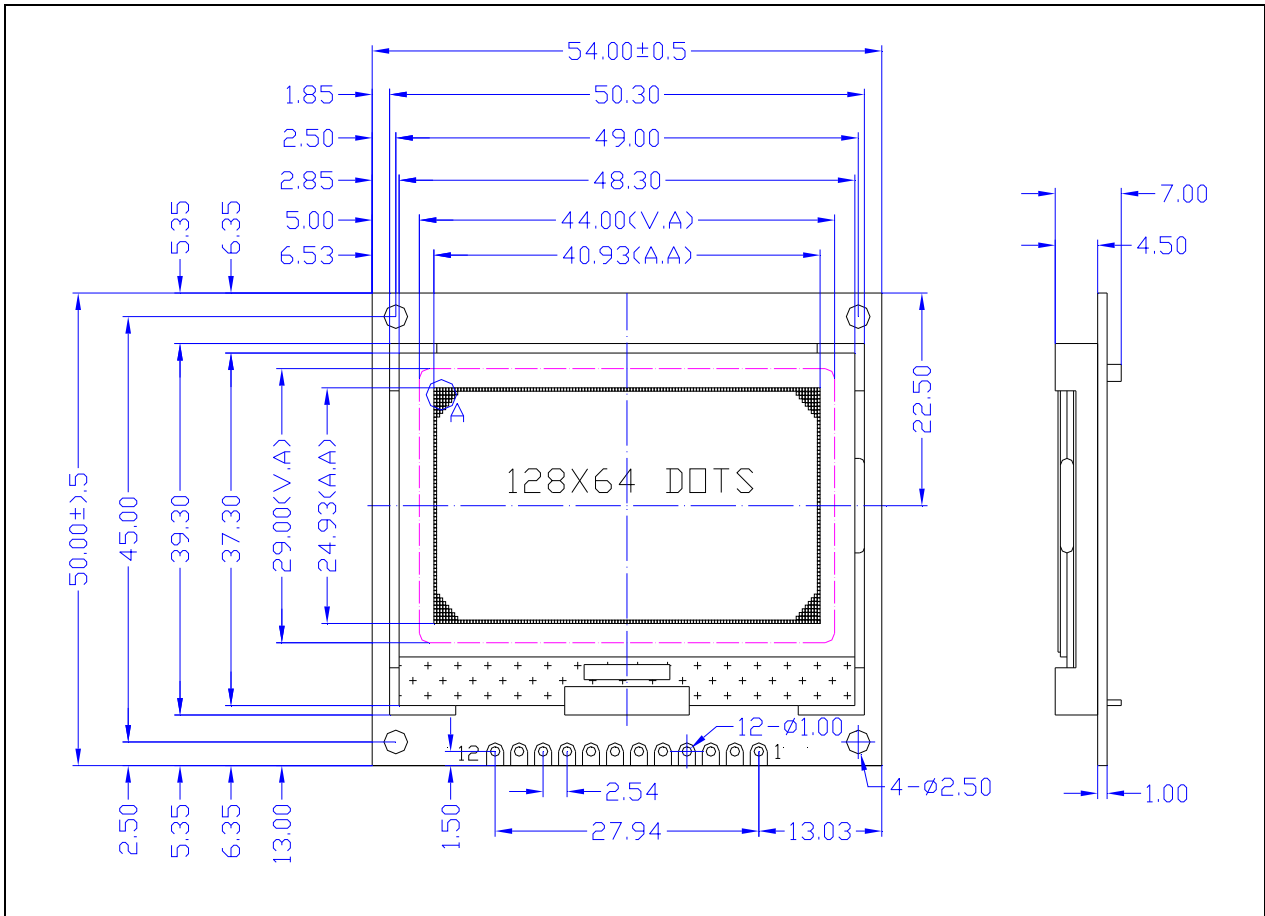


3. 模块外型尺寸:



三：硬件结构图

