北京中显电子有限公司

图形点阵液晶显示模块资料

-ZX240128A- V3.0

公司地址:海淀区中关村大街32号和盛大厦811室

电话:010-82626833 52926620

传真:010-52926621 网址:www.zxlcd.com

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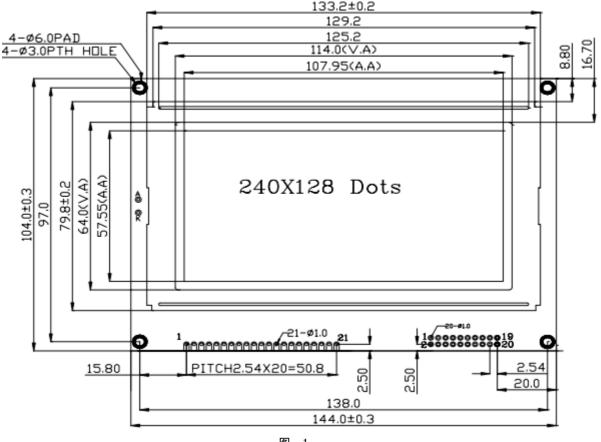
一、概述

ZX240128A是一种图形点阵液晶显示器,它由控制器 T6963C、行驱动器/列驱动器及 240×128 全点阵液晶显示器组成.可完成图形显示,也可以显示 15×8 个(16×16 点阵)汉字.

主要技术参数和性能:

- 1. 电源: VDD: +5V±10%; 模块内可自带-15V 负压,用于 LCD 的驱动电压;
- 2. 显示内容:240(列)×128(行)点;
- 3. 全屏幕点阵;
- 4. 带 8K 外部数据存储器(其地址由软件设定);
- 5. 其接口适配 8080 系列和 Z80 系列 MPU 的控制时序;
- 6. 驱动方式:1/128 DUTY, 1/9 BIAS;
- 7. 工作温度: -10℃ ∽ +60℃(常温), -20℃∽ +70℃ (宽温)存储温度: -30℃ ∽ +80℃;
- 8. 模块可带 LED 或 EL 背光;
- 9.6 0'CLOCK 显示;

二、外形尺寸图





MAX14.0

9.3±0.3

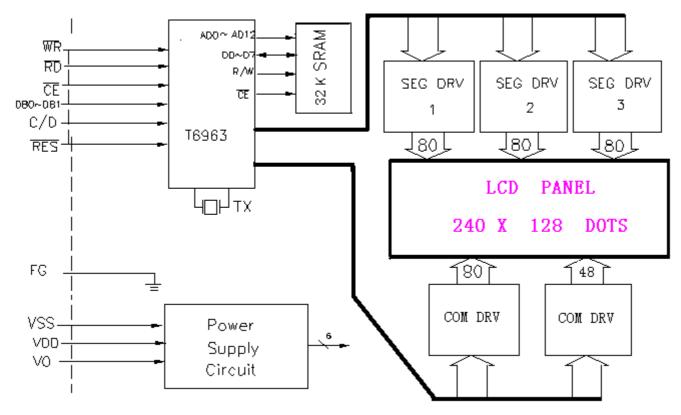
图 1

2. 外形尺寸一览

表 1

1 1		
ITEM	NOMINAL DIMEN	UNIT
模块体积	$144.0 \times 104.0 \times 13.7$	mm
视域	114. 0×64. 0	mm
行列点阵数	240×128	DOTS
点距离	0.40×0.40	mm
点大小	0.45×0.45	mm

三、硬件结构图



四、模块的外部接口

,外部接口信号如下表 2 所示:

表 2

1X 4	0 1 1	7 1	The state of the s
Pin no	Symbol	Level	Function
1	FG	0V	Frame ground
2	Vss	0V	Ground
3	VDD	5.0V	Supply voltage for logic
4	V0	-	Operating voltage for LCD drive
5	/WR	L	Write signal
6	/RD	L	Read signal
7	/CE	L	Chip enable signal
8	C/D	H/L	H: Instruction code L: Data
9	/RESET	L	Reset signal
10	DB0	H/L	Data bit 0
11	DB1	H/L	Data bit 1
12	DB2	H/L	Data bit 2
13	DB3	H/L	Data bit 3
14	DB4	H/L	Data bit 4
15	DB5	H/L	Data bit 5
16	DB6	H/L	Data bit 6
17	DB7	H/L	Data bit 7
18	FS	H/L	Font select signal (H:5X8 dots; L:8X8 dots)
19	VOUT/A		
20	LED+/K	-	LED Backlight
21	LED-		LED Backlight

兼容香港精电 MGLS-240128TA VP240128TA-01 当 19 脚为 LED 背光正 A 时, 20 脚为 LED 背光负 K

五、IC 说明及指令表

T6963C is LCD controller designed to be used for control LCD driver LCD driver LSIs and display data Memory, It has an 8 bit parallel data bus

And control lines for reading or writing through a MPU I/F. It has 128 words character generator ROM with the capability to control External display RAM of up to 128K bytes. Allocation of text, graphics And external generator RAM can be easily made and the display window can Be freely moved within the allocated memory range.

It supports a very board range of LCD formats by selecting different Combinations on a set of programmable inputs. It can be used in text, graphic Modes and has various attribute functions.

指令表:

表 2

COMMAND	CODE	D1	D2	FUNCTION
	00100001	X address	Y address	Cursor pointer set
Register Set	00100010	Data	00H	Off register
	00100100	Low address	High address	Address pointer set
	01000000	Low address	High address	Text home address set
Control	01000001	Columns	00H	Text area set
Word set	01000010	Low address	High address	Graphic home address set
	01000011	Columns	00H	Graphic area set
	1000×000	-	-	"OR" mode
	1000×001	_	-	"EXOR" mode
Mode set	1000x011	_	-	"AND" mode
	1000×100	_	-	"Text attribute" mode
	10000xxx	_	-	Internal CGROM mode
	10001xxx	_	-	External CGRAM mode
	10010000	_	-	Display off
	1001xx10	_	-	Cursor on, blink off
Display	1001xx11	_	-	Cursor on, blink on
Mode	100101xx	_	-	Text on, graphic off
	100110xx	_	-	Text off, graphic on
	100111xx	_	-	Text on, graphic on
	10100000	_	-	1 line cursor
	10100001	_	_	2 line cursor
	10100010	_	-	3 line cursor
Cursor	10100011	_	-	4 line cursor
Pattern	10100100	_	_	5 line cursor
Select	10100101	_	-	6 line cursor
	10100110	_	_	7 line cursor
	10100111	_	_	8 line cursor
Data auto	10110000	_	_	Data auto write set
Read/write	10110001	_	-	Data auto read set
	10110010	_	_	Auto reset
	11000000	Data	-	Data write and ADP increment
	11000001	_	_	Data read and ADP increment
Data read	11000010	Data	_	Data write and ADP decrement
Write	11000011	_	_	Data read and ADP decrement
	11000100	Data	_	Data write and ADP no variable
	11000101	_	_	Data read and ADP no variable
Screen peek	11100000	_	-	Screen peek
Screen copy	11101000	_	_	Screen copy
	11110XX	_	_	Bit reset

	11111XXX	_	-	Bit set
	1111X000	_	_	Bit O(LSB)
	1111X001	_	-	Bit 1
Bit	1111X010	_	_	Bit 2
Set/Reset	1111X011	_	-	Bit 3
	1111X100	_	-	Bit 4
	1111X101	-	-	Bit 5
	1111X110	_	-	Bit 6
	1111X111	_	_	Bit 7(MSB)

六、电气参数

1. ABSOLUTE MAXIMUM RATING

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply	VDD-VSS	-	-0.3	-	7	V
Voltage(logic)						•
Supply	VDD-VO	-	VDD+0.3	-	VDD-0.3	
Voltage(LCD						V
Drive)						
Input Voltage	VI	-	-0.3	-	VDD+0.3	V
Operating	Topr	-	-10	-	+55	° C
Temperature						
Storage	Tstg	-	-20	-	+60	° C
Temperature						

2. OPTICAL DATA Ta=25° C

Item	Symbol	Condition		Condition Standard Value			Unit
				min	typ	max	
Supply voltage(Logic)	VDD-VSS	-		4.75	5	5.25	V
Supply voltage(LCD Drive)	VDD-VO	-		-	-	-	V
Supply current	IDD	-		-	12.0	17.0	mA
	IO	-		-	1.8	2.5	mA
EL Backlight current	IEL	-		-	100	-	mA
Input high voltage	VIH	High	level	0.7VDD	-	VDD	V
Input low voltage	VIL	Low	level	0	-	0.3VDD	V
Supply voltage for LCD		Ta=0	° C	14.2	14.5	14.8	V
Drive (1/80 duty)	VDD-VO	Ta=25	5°C	13.3	13.6	13.9	V
		Ta=50)°C	12.3	12.6	12.9	V
Contrast Ratio	CR			-	4	-	-
Viewing Angle	-	CR≥2	θ	-10	-	20	deg
			θ	60	-	120	deg
Response Time (rise)	Tr	Note 1	Ta=2	-	130	200	ms
			5°				
Response Time (delay)	Td	Note 2	Ta=2	-	150	230	ms
			5°	2 20/			

NOTE 1: Required time for blackening ratio of segment goes up from 0% to 90% when Wave from is switched from one selected one (θ =10° , φ =90°)

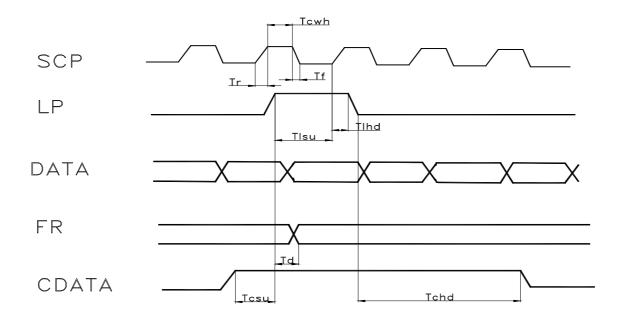
Note 2: Required time for blackening ratio of segment goes down from 100% to 10% When wave from is switched from one selected one (θ =10° , φ =90°)

3. TIMING CHARACTERISICS

Item	Symbol	Min	Max	Unit
Operating frequency	fSCP	-	2.75	MHZ
SCP pulse width	Tewh, Tewl	150		ns
SCP rise/fall time	Tr	-	30	ns
LP set up time	Tlsu	150	290	ns
LP hold time	Tlhd	5	40	ns

Data set up time	Tdsu	170	-	ns
Data hold time	Tdhd	80	-	ns
FR delay time	Td	0	90	ns
CDATA set up time	Tcsu	450	850	ns
CDATA hold time	Tchd	450	950	ns

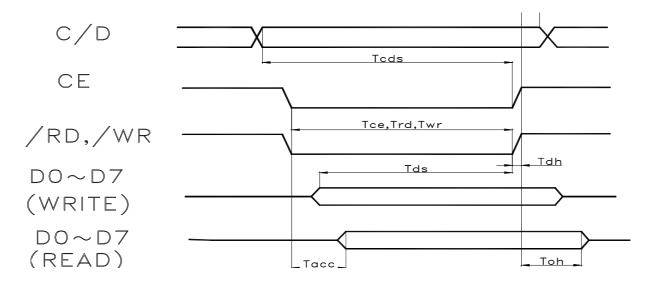
Condition: DV=+5.0V \pm 10%, Ta=-10 \circ +70 $^{\circ}$ C



驱动波形图

4.INTERFACE TIMING

Item	Symbol	Min	Max	Unit
C/D set up time	Tcds	100	-	ns
C/D hold time	Tcdh	10	-	ns
CE,RD,WR pulse width	Tce,Trd,Twr	80	-	ns
DATA set up time	Tds	80	-	ns
DATA hold time	Tdh	40	-	ns
Access time	Tacc	-	150	ns
Output hold time	Toh	10	50	ns



读写时序图

5.EL Backlight Electrical Characteristics

Ta=25° C VDD=5V

Symbol	Parameter	Min	Type	Max	Units
IIN	VDD supply current	10		150	mA
VA-B	Output voltage across lamp	37	40	43	V
		75	80	85	V
FEL	VA-B output drive frequency	600	800	1000	V
VDD	Supply voltage	4.5		12	V
CL	Load capacitance	0		25	nF
TA	Operating temperature	0		50	° C

Note: EL Backlight with white lamp.

七、功能描述

7.1 STATUS READ

Before sending data(read/write), command it is necessary to check the Status.

Status of T6963c can read from data lines.

/RD L /WR Н /CE L C/DН

D0∽D7 Status word

T6963C status word format is following

MOR						LDD	
STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

I CD

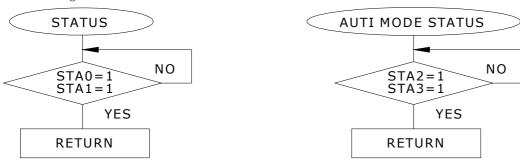
STA0	Check capability of command execution	0:Disable 1:Enable
STA1	Check capability of data read/write	0:Disable 1:Enable
STA2	Check capability of auto mode data read	0:Disable 1:Enable
STA3	Check capability of auto mode data write	0:Disable 1:Enable
STA4	Not use	
STA5	Check capability of controller operation	0:Disable 1:Enable
STA6	Error flag. Using screen peek/copy command	0:No error 1:error
STA7	Check the condition blink	0:Disable 1:Enable

Note 1:It is necessary to check STAO and STAO at the same time, The error

Is happened by sending data at executing command. 2: The status check will be enough to check STAO/STA1.

3:STA2/STA3 are valid in auto mode STA0/STA1 are invalid.

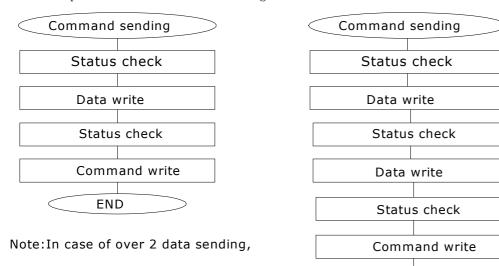
Status checking flow:



7.2 DATA SET

In T6963C, the data have been set and command executes.

The order of procedure of command sending



7.31 Description of command

1. Register set

valid).

CODE	HEX	FUNCTION	D1	D2
00100001	21H	Cursor pointer set	X address	Y address
00100010	22H	Offset register set	Data	00Н
00100100	24H	Address pointer set	Low address	High address

END

Cursor pointer set

The position of cursor is specified by X address. The cursor position Is moved only by this command. The cursor pointer doesn't have the Function of increment and decrement. The shift of cursor set by this Command. X address, Y address are specified following. X address 00H - - - 4FH (Low 7bits are valid)

00H - - - - 1FH (Low 5bits are valid) address

Screen drive

X address 00H - - - 4FH

Y address 00H - - - - 0FH

Offset register set

The offset register is used to determine external character generator

T6963C has 16 bits address lines as follow:

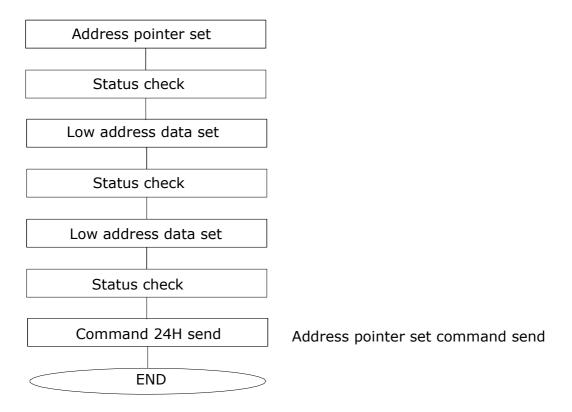
Ad15 Ad14 Ad13 Ad12 Ad11 Ad10 Ad9 Ad8 Ad7 Ad6 Ad5 Ad4 Ad3 Ad2 Ad1 Ad0	MOD											LC	D D		
	Ad15	ACL3	Ad12	Adl1	Ad10	Ad9	Ad8	Ad7	Ad6	Ad5	Ad4	Ad3	Ad2	Adl	Ad0

The upper 5 bits (ad15 - ad11) are determined by offset register. The Middle 8 bits (ad10 - ad3) are determined by character code. The Lower 3 bit (ad2 - ad10) are determined by vertical counter. The Lower 5 bit of D1(data) are valid. The data format of external character Generator RAM.

(3) Address pointer set

The address pointer set command is used to indication the start address For writing (or reading) to external RAM.

The flow chart address pointer set command



7.32 Control word set

1.02 00110101	TOTA DOC			
CODE	HEX	FUNCTION	D1	D2
01000000	40H	Text home address set	Low address	High address
01000001	41H	Text area set	Columns	00Н
01000010	42H	Graphic home address set	Low address	High address
01000011	43H	Graphic area set	Columns	00H

The home address and column size are defined by this command

(1) Text home address and area set

The starting address of external display RAM for text display is Defined by this command. The text home address shows the left end and Most upper position .

The relationship of external display RAM address and display position

Example:

Text home address: 0000H
Text area: 00A0H
MD2=0, MD3=0: 80 COLUMN
DUAL=0, MDS=1, MD0=1, MD1=0: 28 LINES

Display plane:

1 7 1					
0000Н	0001H		004EH	004FH	1 Line
ООАОН	00A1H		00EEH	00EFH	2 Line
:	:	:	:	:	:
:	:	:	:	:	:
:	:	:	:	:	:
10E0H	10E1H		112EH	112FH	28 Lines

(2) Graphic home address and area set

The starting address of external display RAM for Graphic display is Defined by this command. The graphic home address shows the left end most Upper line.

The relationship of external display RAM address and display position.

Example: Graphic home address: Graphic area:
MD2=H, MD3=H:
DUAL=H, MDS=L, MD0=H, MD1=H: 0020H32 COLUMNS 2 LINES

Example:

Display plane:

propray prame.				
Н0000	0001H		001EH	001FH
0020Н	0021H		003EH	003FH
:	:	;	;	:
:	;	:	;	:
:	;	:	;	:
01E0H	01E1H		01FEH	01FFH

7.33 MODE SET

The display mode is defined by this command. The display mode don't have changed until to send next this command. Logically "OR", "EXOR', "AND" of text and graphic display can be displayed.

When internal character generator mode is selected, character code 00H - 7FH are selected from built—in character generator ROM. The character code 80H-FFH are automatically selected external character generator RAM.

NOTE: Only text display is attributed, because attributed data is located.

Attribute function

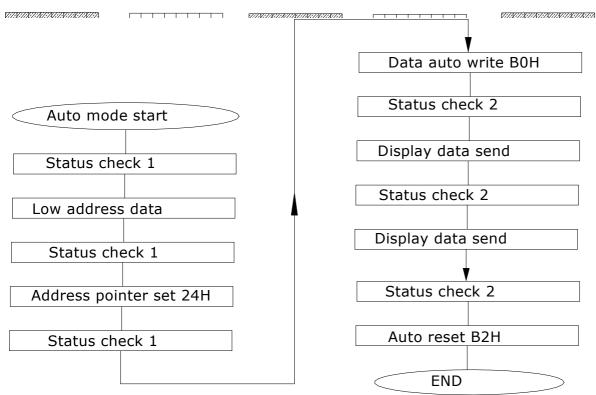
"Reverse display", "Character blink" and "Inhibit" are called "Attribute". The attribute data is written in the graphic area defined by control word set command. The mode set command selects text display only and graphic the mode set command selects text display only and graphic display cannot be displayed. The attribute data of the location of the character in text area 1^{SI} character in text area Is written at the IST 1 byte in graphic area, and attribute data of n-th 1byte in graphic area. Attribute function

is defined as follow. Attribute RAM 1byte

TICULI	LDUICO I	um 10	<i>y</i> 00				
X	X	X	X	D3	D2	D1	D0

X: don't care

D3	D2	D1	D0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	0	Blink of reverse display
1	0	1	1	Blink of inhibit display



7.34 DATA AUTO READ/WRITE

Ī	CODE	HEX	FUNCTION	OPERAND
ſ	10110000	ВОН	Data auto write set	_
ſ	10110001	B1H	Data auto read set	_

Auto reset

This command is convenient to send full screen data from external display RAM. After setting auto mode, "Data write (or read)" command is not necessary between each data. "Data auto write (or read)" command should follow the "Address pointer set" and address pointer is automatically increment by + 1 after each data. After sending (or receiving) all data "Auto reset" is necessary to return normal operation because all data is regarded "Display data" and no command

can be accepted in the auto mode.

Note: status check for auto mode(STA2, STA3 should be checked between each Data. Auto reset should be performed after checking (STA3=1 STA2=1)

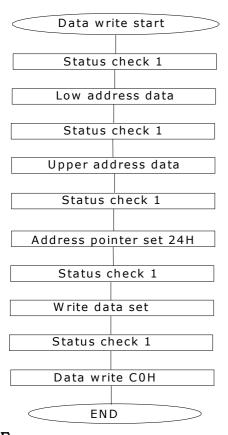
7.35 DATA READ WRITE

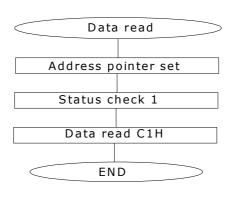
CODE	HEX	FUNCTION	OPERAND
11000000	СОН	Data write and ADP increment	Data
11000001	C1H	Data read and ADP increment	Data
11000010	C2H	Data write and ADP decrement	Data
11000011	СЗН	Data read and ADP decrement	Data
11000100	C4H	Data write and ADP nonvariable	Data
11000101	С5Н	Data read and ADP nonvariable	Data

This command is used for data write from MPU to external display RAM, AND data read external display RAM to MPU. Data write/data read should be executed after setting address by address pointer set command. Address Pointer can be automatically increment by setting this command.

Note: this command is necessary for each 1 byte data.

Please refer following flow chart.





NOTE:

- (1) After power on, it is necessary to reset. /RESET is kept "L" between 5 CLOCK up (oscillation clock).
- When /HALT has been "L", the oscillation is stopped. It is necessary To turn off power supply for LCD, because LCD goes down by DC bias.
- (3) The HALF function contains the RESET function.
- (4) After state of RESET/HALT.

TERMINAL	HALT	RESET
D0-D7	F	F
D0-d7	F	F
R/w	Н	Н
/ce	H (NOTE 1)	H (NOTE 1)
Ad0-ad15	H (NOTE 2)	H (NOTE 2)

/ce0, /ce1	H (NOTE 1)	H (NOTE 1)
ED, HOD	Final Data	Final Data
HSCP	L	L
LP	L	L
CDATA	Н	Н
FR	Н	Н
CH1	L	КО
CH2	L	VEND
DSPON	L	L
XO	Н	OSC CLOCK

H: Level H

L: Level L

F: Floating (High impedance)

KO: Internal state (TEXT data access) normally open VEND: End signal of V-counter(Line count) if MDS=H, T2=L,

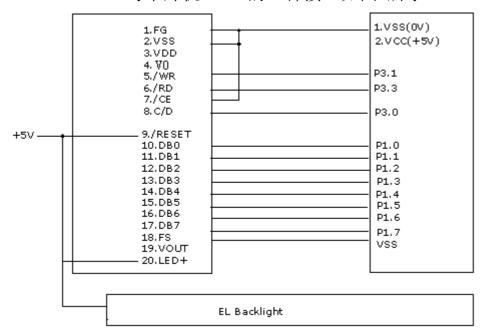
HEND (end signal of H-count) normally open.

Note 1: In Attribute mode, H or L by state of Graphinc pointer.

Note 2: In Attribute mode, DATA of Graphinc pointer.

八、应用举例

ZX240128A 与单片机 8031 的一种接口如图. 所示:



注:V0 为液晶驱动电压。此图为模块内自带负压的示例

ORG 0000H AJMP MAIN

ORG 0003H

AJMP ZHONGDUAN

ORG 0035H

ZHONGDUAN:

HERE: SJMP HERE

RET

DATA1 EQU 30H ;第一参数单元 DATA2 EQU 31H ;第二参数/数据单元 COMMAND EQU 32H ;指令代码单元

C_ADD EQU 8100H ;指令通道地址

```
:数据通道地址
    D ADD
          EOU
                 8000H
    LI1
          EQU
                33H
    LI2
         EQU
                34H
BUSY1:
    PUSH
          DPH
    PUSH
          DPL
    MOV
          DPTR,#C_ADD
    MOVX
           A,@DPTR
    POP
         DPL
    POP
         DPH
    RET
BUSY: LCALL
             BUSY1
       JNB
            ACC.0, BUSY1
      JNB
            ACC.1, BUSY1
      RET
*************
  WRITE_COMMAND:PUSH
                      DPH
           PUSH
                  DPL
           LCALL
                   BUSY
           MOV
                   A,COMMAND
           MOV
                   DPTR,#C_ADD
           MOVX
                   @DPTR,A
           POP
                  DPL
                  DPH
           POP
           RET
  WRITE_DATA:
           PUSH
                  DPH
           PUSH
                  DPL
           LCALL
                   BUSY
           MOV
                   A,DATA2
           MOV
                   DPTR,#D_ADD
           MOVX
                   @DPTR,A
           POP
                  DPL
           POP
                  DPH
           RET
  MS40:
        MOV
              R7,#0E8H
  MS2:
        MOV
              R6,#0FFH
  MS1:
        DJNZ
              R6,MS1
        DJNZ
             R7,MS2
        RET
   DELAY: MOV
              R5,#05H
   DELAY1: LCALL MS40
        DJNZ
            R5,DELAY1
        RET
MAIN:
       MOV
             SP,#20H
             P3,#0FFH
       MOV
       SETB
             EΑ
       SETB
             EX0
       SETB
             IT0
             COMMAND,#90H
       MOV
       LCALL WRITE_COMMAND
       LCALL MS40
       MOV
             DATA2,#00H
       LCALL WRITE_DATA
```

MOV

DATA2,#00H

```
;文本显示区首地址设置
     MOV
           COMMAND.#40H
     LCALL WRITE_COMMAND
    ; MOV
           DATA2,#20H
     MOV
           DATA2,#20H
     LCALL WRITE_DATA
     MOV
           DATA2,#00H
     LCALL WRITE_DATA
     MOV
           COMMAND,#41H
                        :文本显示区宽度设置
     LCALL WRITE_COMMAND
     MOV
           DATA2,#00H
     LCALL WRITE DATA
     MOV
           DATA2,#08H
     LCALL WRITE_DATA
           COMMAND,#42H ;图形显示区首地址设置
     MOV
     LCALL WRITE_COMMAND
     MOV
           DATA2,#1EH
     LCALL WRITE_DATA
     MOV
           DATA2,#00H
     LCALL WRITE DATA
     MOV
           COMMAND,#43H ;图形显示宽度设置
     LCALL WRITE COMMAND
     MOV
           COMMAND,#0A1H ; 光标形状
     LCALL WRITE_COMMAND
     MOV
           COMMAND,#80H
                         ;逻辑或,用 CGROM
     LCALL WRITE COMMAND
     MOV
           COMMAND,#9CH ;
                            启动文本.图形.光标显示
     LCALL WRITE_COMMAND
;显示全屏
      MOV DATA2,#00H
      LCALL WRITE_DATA
      MOV DATA2,#00H
      LCALL WRITE DATA
      MOV COMMAND,#24H
      LCALL WRITE_COMMAND
      MOV
          R3,#00H
      MOV COMMAND,#0B0H
      LCALL WRITE_COMMAND
      MOV
            R4,#20H
 YY1:
      NOP
 YY:
      MOV
            A,#0FFH
      MOV
          DATA2.A
      LCALL WRITE_DATA
      DJNZ
           R3,YY
      DJNZ
           R4,YY1
            COMMAND,#0B2H
      MOV
      LCALL WRITE_COMMAND
      LCALL MS40
```

LCALL WRITE_DATA

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```
CLEAR1:
     MOV
           DATA2,#00H
     LCALL
           WRITE_DATA
     MOV
           DATA2,#00H
     LCALL
           WRITE DATA
           COMMAND,#24H
                           ;显示地址设置
     MOV
     LCALL
           WRITE COMMAND
     MOV
           R3,#00H
     MOV
           R4,#20H
     MOV
                           ;自动写入
           COMMAND,#0B0H
     LCALL WRITE_COMMAND
  M1:
     NOP
  M: MOV
           A,#00H
     MOV
           DATA2,A
     LCALL WRITE DATA
           R3,M
     DJNZ
     DJNZ
           R4,M1
     MOV
                           ;推出自动写入
           COMMAND,#0B2H
     LCALL WRITE COMMAND
    ************
  ;显示字库
     MOV
           R3,#00H
     MOV
           R4,#7FH
                           ;自动写入
     MOV
           COMMAND,#0B0H
     LCALL
           WRITE COMMAND
     MOV
           A,#0FFH
                         ;写入英文字库
  L: INC
          Α
     MOV
           DATA2,A
     LCALL WRITE DATA
     DJNZ
          R4,L
           COMMAND,#0B2H
                           ;退出自动写入
     MOV
     LCALL WRITE_COMMAND
     LCALL DELAY
; 写 CGROM 80H
     MOV
           DATA2,#03H
     LCALL
           WRITE_DATA
           DATA2,#00H
     MOV
     LCALL
           WRITE_DATA
     MOV
           COMMAND,#22H
     LCALL WRITE COMMAND
     MOV
           DATA2,#00H
     LCALL
           WRITE_DATA
     MOV
           DATA2,#1CH
     LCALL
           WRITE_DATA
     MOV
           COMMAND,#24H
```

LCALL WRITE_COMMAND

```
MOV
           R2,#2
     MOV
           R0,#0FFH
     MOV
           COMMAND,#0B0H
     LCALL WRITE_COMMAND
     MOV
           DPTR,#TEST
LL2:
     NOP
LL:
     CLR
          Α
     MOVC
            A, @A+DPTR
     MOV
            DATA2,A
     LCALL WRITE_DATA
     INC
           DPTR
     SETB
           P1.5
     DJNZ
           R0,LL
     DJNZ
           R2,LL2
            DATA2,#0B2H
     MOV
     LCALL WRITE DATA
     SETB
           P1.4
MOV
            DATA2,#00H
           WRITE_DATA
     LCALL
            DATA2,#00H
     MOV
     LCALL WRITE_DATA
     MOV
            COMMAND,#24H
                            :显示地址设置€
     LCALL WRITE_COMMAND
     MOV
           COMMAND,#0B0H
     LCALL WRITE COMMAND
     MOV
           R2,#2
TIAO1: MOV
           R1,#255
      MOV
MMM:
           DATA2,#80H
     LCALL WRITE DATA
     DJNZ
           R1,MMM
     DJNZ
           R2,TIAO1
     MOV
           COMMAND,#0B2H
     LCALL WRITE COMMAND
     LCALL DELAY
     MOV
           COMMAND,#0B0H
     LCALL WRITE COMMAND
     MOV
            DATA2,#00H
     LCALL WRITE_DATA
     MOV
            DATA2,#00H
     LCALL WRITE_DATA
     MOV
            COMMAND.#24H
                            ;显示地址设置€
     LCALL WRITE_COMMAND
     MOV
           R2,#2
TIAO2: MOV
           R1,#255
MMM1: MOV DATA2,#81H
     LCALL WRITE DATA
     DJNZ R1,MMM1
     DJNZ R2,TIAO2
     MOV
          COMMAND,#0B2H
     LCALL WRITE COMMAND
     LCALL DELAY
```

MOV COMMAND,#0B0H LCALL WRITE_COMMAND MOV DATA2,#00H LCALL WRITE DATA MOV DATA2,#00H LCALL WRITE_DATA MOV COMMAND,#24H ;显示地址设置€ LCALL WRITE_COMMAND MOV R2,#2 TIAO3: MOV R1,#255 MMM2: MOV DATA2,#82H LCALL WRITE_DATA DJNZ R1,MMM2 DJNZ R2,TIAO3 MOV COMMAND,#0B2H LCALL WRITE COMMAND LCALL DELAY MOV COMMAND,#0B0H LCALL WRITE_COMMAND MOV DATA2,#00H LCALL WRITE DATA MOV DATA2,#00H LCALL WRITE_DATA MOV ;显示地址设置€ COMMAND,#24H LCALL WRITE COMMAND MOV R2,#2 TIAO4: MOV R1,#255 NN: MOV DATA2,#83H LCALL WRITE_DATA DJNZ R1,NN DJNZ R2,TIAO4 MOV COMMAND,#0B2H LCALL WRITE COMMAND LCALL DELAY MOV COMMAND,#0B0H LCALL WRITE_COMMAND MOV DATA2,#00H LCALL WRITE DATA MOV DATA2,#00H LCALL WRITE_DATA MOV COMMAND,#24H ;显示地址设置€ LCALL WRITE COMMAND MOV R2,#2 TIAO5: MOV R1,#255 NN1: MOV DATA2,#84H LCALL WRITE_DATA DJNZ R1,NN1 DJNZ R2,TIAO5 MOV COMMAND,#0B2H LCALL WRITE_COMMAND LCALL DELAY MOV COMMAND,#0B0H LCALL WRITE_COMMAND

MOV

DATA2,#00H

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```
LCALL WRITE_DATA
        MOV
               DATA2,#00H
        LCALL
              WRITE_DATA
                               ;显示地址设置€
        MOV
               COMMAND,#24H
        LCALL WRITE_COMMAND
        MOV
              R2,#2
  TIAO6: MOV
              R1,#255
  NN2:
        MOV
              DATA2,#85H
        LCALL WRITE_DATA
        DJNZ R1,NN2
        DJNZ R2,TIAO6
        MOV
              COMMAND,#0B2H
        LCALL WRITE_COMMAND
        LCALL DELAY
        MOV
               DATA2,#00H
        LCALL WRITE_DATA
               DATA2,#00H
        MOV
        LCALL WRITE_DATA
        MOV
               COMMAND,#24H
                               ;显示地址设置
        LCALL
              WRITE_COMMAND
        MOV
               R3,#00H
        MOV
               R4,#20H
        MOV
               COMMAND,#0B0H
                                ;自动写入
        LCALL
              WRITE COMMAND
    MM1: MOV
               R4.#20H
    MM: MOV
               A,#00H
        MOV
               DATA2,A
        LCALL WRITE_DATA
        DJNZ
              R4,MM
        DJNZ
              R3,MM1
        MOV
                                ;推出自动写入
               COMMAND,#0B2H
        LCALL WRITE_COMMAND
TIAN:
       MOV
                            ;显示地址
              DATA2,#00H
       LCALL WRITE_DATA
       MOV
              DATA2,#08H
       LCALL WRITE_DATA
       MOV
              COMMAND.#24H
       LCALL WRITE_COMMAND
       MOV
              COMMAND,#0B0H
       LCALL
             WRITE_COMMAND
       MOV
              R2,#10H
       MOV
              R1,#000H
       MOV
              DPTR,#TIANSHI
  SSS:
        NOP
  SSS1:
        CLR
       MOVC
              A.@A+DPTR
       MOV
              DATA2,A
       LCALL WRITE_DATA
       INC
             DPTR
       DJNZ
             R1.SSS1
       DJNZ
             R2,SSS
```

MOV

COMMAND,#0B2H

LCALL WRITE_COMMAND LCALL DELAY LJMP MAIN

TIANSHI:

DB DB Н000, H000, DB H000, DB H000, DB H000, H000DB DB DB DB 000H,000H,000H,001H,080H,020H,010H,080H,004H,000H,000H,040H,010H,000H,001H,000H DB 010H,000H,004H,000H,020H,010H,080H,004H,000H,000H,040H,010H,000H,001H,000H,010H DB 000H,001H,081H,024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H,0BFH,080H,090H,01FH DB 0FEH,001H,024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H,0BFH,080H,090H,01FH,0FDH DB 0FDH,024H,057H,0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H,000H,090H,000H,004H,07DH DB 024H,057H,0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H,000H,090H,000H,005H,091H,0FCH DB 050H,080H,07FH,0F0H,001H,000H,020H,004H,0BFH,001H,008H,000H,024H,011H,0FCH,050H 080H,07FH,0F0H,001H,000H,020H,004H,0BFH,001H,008H,000H,025H,090H,000H,078H,080H DB 044H,020H,006H,000H,041H,005H,021H,002H,008H,03FH,0F4H,010H,000H,078H,080H,044HDB 020H,006H,000H,041H,005H,021H,002H,008H,03FH,0F5H,093H,0FEH,097H,0FCH,044H,020H DB DB 004H,020H,0FFH,085H,021H,004H,004H,000H,004H,013H,0FEH,097H,0FCH,044H,020H,004H DB 020H,0FFH,085H,021H,004H,004H,000H,005H,0FCH,040H,090H,020H,07FH,0E3H,0FFH,0F1H 041H,004H,0BFH,008H,043H,08FH,0C4H,07CH,040H,090H,020H,07FH,0E3H,0FFH,0F1H,041H DB DB 004H,0BFH,008H,043H,08FH,0C5H,093H,0FCH,018H,020H,044H,020H,004H,002H,07FH,004H 0A8H,090H,041H,008H,044H,013H,0FCH,018H,020H,044H,020H,004H,002H,07FH,004H,0A8H DB DB 090H,041H,008H,045H,092H,094H,037H,0FCH,044H,020H,004H,004H,004H,004H,0A9H,000H 080H,008H,044H,012H,094H,037H,0FCH,044H,020H,004H,004H,041H,004H,0A9H,000H,080H DB 008H,045H,092H,094H,0D2H,020H,07FH,0E0H,004H,000H,07FH,006H,0AAH,000H,090H,00FH DB 0C4H,012H,094H,0D2H,020H,07FH,0E0H,004H,000H,07FH,006H,0AAH,000H,090H,00FH,0C5H DB DB 09AH,094H,011H,020H,044H,000H,004H,000H,041H,005H,024H,001H,008H,008H,044H,01AH 094H,011H,020H,044H,000H,004H,000H,041H,005H,024H,001H,008H,008H,045H,0E2H,094H DB 010H,020H,004H,008H,004H,000H,041H,004H,022H,002H,004H,000H,004H,062H,094H,010H DB DB 020H,004H,008H,004H,000H,041H,004H,002H,002H,004H,000H,005H,082H,094H,010H,0A0H DB 004H,008H,014H,000H,041H,004H,039H,0C7H,0FEH,000H,014H,002H,094H,010H,0A0H,004H 008H,014H,000H,041H,004H,039H,0C7H,0FEH,000H,015H,082H,00CH,010H,040H,003H,0F8H DB DB 008H,000H,043H,004H,020H,082H,002H,000H,008H,002H,00CH,010H,040H,003H,0F8H,008H DB DB H000, H000DB H000, H080, H100, H000, H000H000, H000, H000DB DB Н000, DB 000H,001H,080H,020H,010H,080H,004H,000H,000H,010H,010H,000H,001H,000H,010H,000H DB DB 081H,024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H,0BFH,080H,090H,01FH,0FEH,001H DB DB 024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H,0BFH,080H,090H,01FH,0FDH,0FDH,024H DB 057H,0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H,000H,090H,000H,004H,07DH,024H,057H DB 0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H,000H,090H,000H,005H,091H,0FCH,050H,080H DB 07FH,0F0H,001H,000H,020H,004H,0BFH,001H,008H,000H,024H,011H,0FCH,050H,080H,07FH 0F0H,001H,000H,020H,004H,0BFH,001H,008H,000H,005H,090H,000H,078H,080H,044H,020H DB DB 006H,000H,041H,005H,021H,002H,008H,03FH,0F4H,010H,000H,078H,080H,044H,020H,006H 000H,041H,005H,021H,002H,008H,03FH,0F5H,093H,0FEH,097H,0FCH,044H,020H,004H,020H DB DB 0FFH,085H,021H,004H,004H,000H,004H,013H,0FEH,097H,0FCH,044H,020H,004H,020H,0FFH DB 085H,021H,004H,004H,000H,005H,0FCH,040H,090H,020H,07FH,0E3H,0FFH,0F1H,041H,004H

DB 0BFH,008H,043H,08FH,0C4H,07CH,040H,090H,020H,07FH,0E3H,0FFH,0F1H,041H,004H,0BFH DB 008H,043H,08FH,0C5H,093H,0FCH,018H,020H,044H,020H,004H,002H,07FH,004H,0A8H,090H 041H,008H,044H,013H,0FCH,018H,020H,044H,020H,004H,002H,07FH,004H,0A8H,090H,041H DB DB 008H,045H,092H,094H,037H,0FCH,044H,020H,004H,004H,041H,004H,0A9H,000H,080H,008H DB 044H,012H,094H,037H,0FCH,044H,020H,004H,004H,041H,004H,0A9H,000H,080H,008H,045H 092H.094H.0D2H.020H.07FH.0E0H.004H.000H.07FH.006H.0AAH.000H.090H.00FH.0C4H.012H DB 094H,0D2H,020H,07FH,0E0H,004H,000H,07FH,006H,0AAH,000H,090H,00FH,0C5H,09AH,094H DB DB 011H,020H,044H,000H,004H,000H,041H,005H,024H,001H,008H,008H,044H,01AH,094H,011H DB 020H,044H,000H,004H,000H,041H,005H,024H,001H,008H,008H,045H,0E2H,094H,010H,020H DB 004H,008H,004H,000H,041H,004H,022H,002H,004H,000H,004H,062H,094H,010H,020H,004H 008H,004H,000H,041H,004H,002H,002H,004H,000H,005H,082H,094H,010H,0A0H,004H,008H DB 014H,000H,041H,004H,039H,0C7H,0FEH,000H,014H,002H,094H,010H,0A0H,004H,008H,014H DB 000H,041H,004H,039H,0C7H,0FEH,000H,015H,082H,00CH,010H,040H,003H,0F8H,008H,000H DB 043H,004H,020H,082H,002H,000H,008H,002H,00CH,010H,040H,003H,0F8H,008H,000H,043H DB DB H000, H000DB DB DB DB H000, H080, H100, H000 DB H000, DB DB DB DB 004H,000H,0FFH,0E0H,010H,047H,0BFH,080H,090H,01FH,0FEH,001H,024H,010H,080H,004H DB 000H,0FFH,0E0H,010H,047H,0BFH,080H,090H,01FH,0FDH,0FDH,024H,057H,0F8H,004H,020H DB DB 000H,087H,0FFH,0E4H,0A1H,000H,090H,000H,004H,07DH,024H,057H,0F8H,004H,020H,000H DB 087H,0FFH,0E4H,0A1H,000H,090H,000H,005H,091H,0FCH,050H,080H,07FH,0F0H,001H,000H 020H,004H,0BFH,001H,008H,000H,024H,011H,0FCH,050H,080H,07FH,0F0H,001H,000H,020H DB DB 004H,0BFH,001H,008H,000H,005H,090H,000H,078H,080H,044H,020H,006H,000H,041H,005H 021H,002H,008H,03FH,0F4H,010H,000H,078H,080H,044H,020H,006H,000H,041H,005H,021H DB DB 002H,008H,03FH,0F5H,093H,0FEH,097H,0FCH,044H,020H,004H,020H,0FFH,085H,021H,004H 004H,000H,004H,013H,0FEH,097H,0FCH,044H,020H,004H,020H,0FFH,085H,021H,004H,004H DB 000H,005H,0FCH,040H,090H,020H,07FH,0E3H,0FFH,0F1H,041H,004H,0BFH,008H,043H,08FH DB DB 0C4H,07CH,040H,090H,020H,07FH,0E3H,0FFH,0F1H,041H,004H,0BFH,008H,043H,08FH,0C5H DB 093H,0FCH,018H,020H,044H,020H,004H,002H,07FH,004H,0A8H,090H,041H,008H,044H,013H 0FCH,018H,020H,044H,020H,004H,002H,07FH,004H,0A8H,090H,041H,008H,045H,092H,094H DB 037H,0FCH,044H,020H,004H,004H,041H,004H,0A9H,000H,080H,008H,044H,012H,094H,037H DB DB 0FCH,044H,020H,004H,004H,041H,004H,0A9H,000H,080H,008H,045H,092H,094H,0D2H,020H 07FH,0E0H,004H,000H,07FH,006H,0AAH,000H,090H,00FH,0C4H,012H,094H,0D2H,020H,07FH DB 0E0H,004H,000H,07FH,006H,0AAH,000H,090H,00FH,0C5H,09AH,094H,011H,020H,044H,000H DB DB 004H,000H,041H,005H,024H,001H,008H,008H,004H,01AH,094H,011H,020H,044H,000H,004H DB 000H,041H,005H,024H,001H,008H,008H,045H,0E2H,094H,010H,020H,004H,008H,004H,000H 041H,004H,022H,002H,004H,000H,004H,062H,094H,010H,020H,004H,008H,004H,000H,041H DB DB 004H,022H,002H,004H,000H,005H,082H,094H,010H,0A0H,004H,008H,014H,000H,041H,004H 039H,0C7H,0FEH,000H,014H,002H,094H,010H,0A0H,004H,008H,014H,000H,041H,004H,039H DB 0C7H,0FEH,000H,015H,082H,00CH,010H,040H,003H,0F8H,008H,000H,043H,004H,020H,082H DB 002H,000H,008H,002H,00CH,010H,040H,003H,0F8H,008H,000H,043H,004H,020H,082H,002H DB Н000, DB DB H000, H080 DB DB DB H000, DB H000, H000, H000, H080, H100, H000, DB DB DB 040H,010H,000H,001H,000H,010H,000H,001H,081H,024H,010H,080H,004H,000H,0FFH,0E0HDB DB 010H,047H,0BFH,080H,090H,01FH,0FEH,001H,024H,010H,080H,004H,000H,0FFH,0E0H,010H DB 047H,0BFH,080H,090H,01FH,0FDH,0FDH,024H,057H,0F8H,004H,020H,000H,087H,0FFH,0E4H

0A1H,000H,090H,000H,004H,07DH,024H,057H,0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H DB DB 000H,090H,000H,005H,091H,0FCH,050H,080H,07FH,0F0H,001H,000H,020H,004H,0BFH,001H 008H,000H,024H,011H,0FCH,050H,080H,07FH,0F0H,001H,000H,020H,004H,0BFH,001H,008H DB DB 000H,005H,090H,000H,078H,080H,044H,020H,006H,000H,041H,005H,021H,002H,008H,03FH 0F4H,010H,000H,078H,080H,044H,020H,006H,000H,041H,005H,021H,002H,008H,03FH,0F5H DB 093H.0FEH.097H.0FCH.044H.020H.004H.020H.0FFH.085H.021H.004H.004H.000H.004H.013H DB 0FEH,097H,0FCH,044H,020H,004H,020H,0FFH,085H,021H,004H,004H,000H,005H,0FCH,040H DB DB 090H,020H,07FH,0E3H,0FFH,0F1H,041H,004H,0BFH,008H,043H,08FH,0C4H,07CH,040H,090H DB 020H,07FH,0E3H,0FFH,0F1H,041H,004H,0BFH,008H,043H,08FH,0C5H,093H,0FCH,018H,020H DB 044H,020H,004H,002H,07FH,004H,0A8H,090H,041H,008H,044H,013H,0FCH,018H,020H,044H 020H,004H,002H,07FH,004H,0A8H,090H,041H,008H,045H,092H,094H,037H,0FCH,044H,020H DB 004H,004H,004H,004H,00A9H,000H,080H,008H,044H,012H,094H,037H,0FCH,044H,020H,004H DB DB 004H,041H,004H,0A9H,000H,080H,008H,045H,092H,094H,0D2H,020H,07FH,0E0H,004H,000H 07FH,006H,0AAH,000H,090H,00FH,0C4H,012H,094H,0D2H,020H,07FH,0E0H,004H,000H,07FH DB DB 006H,0AAH,000H,090H,00FH,0C5H,09AH,094H,011H,020H,044H,000H,004H,000H,041H,005H 024H,001H,008H,008H,044H,01AH,094H,011H,020H,044H,000H,004H,000H,041H,005H,024H DB 001H,008H,008H,045H,0E2H,094H,010H,020H,004H,008H,004H,000H,041H,004H,022H,002H DB DB 004H,000H,004H,062H,094H,010H,020H,004H,008H,004H,000H,041H,004H,022H,002H,004H DB 000H,005H,082H,094H,010H,0A0H,004H,008H,014H,000H,041H,004H,039H,0C7H,0FEH,000H DB 014H,002H,094H,010H,0A0H,004H,008H,014H,000H,041H,004H,039H,0C7H,0FEH,000H,015H 082H,00CH,010H,040H,003H,0F8H,008H,000H,043H,004H,020H,082H,002H,000H,008H,002H DB DB 00CH,010H,040H,003H,0F8H,008H,000H,043H,004H,020H,082H,002H,000H,009H,080H,000H DB H000, H080, H100, H000, DB H000, DB H000, H000, H000, H000, H000, H000, H000, H100, H000, DB DB DB 000H,000H,000H,000H,000H,000H,000H,001H,080H,020H,010H,080H,004H,000H,000H,040H 010H,000H,001H,000H,010H,000H,004H,000H,020H,010H,080H,004H,000H,000H,040H,010H DB DB 000H,001H,000H,010H,000H,001H,081H,024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H 0BFH,080H,090H,01FH,0FEH,001H,024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H,0BFH DB DB 080H,090H,01FH,0FDH,0FDH,024H,057H,0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H,000H 090H,000H,004H,07DH,024H,057H,0F8H,004H,020H,000H,087H,0FFH,0E4H,0A1H,000H,090H DB 000H,005H,091H,0FCH,050H,080H,07FH,0F0H,001H,000H,020H,004H,0BFH,001H,008H,000H DB DB 024H,011H,0FCH,050H,080H,07FH,0F0H,001H,000H,020H,004H,0BFH,001H,008H,000H,005H DB 090H,000H,078H,080H,044H,020H,006H,000H,041H,005H,021H,002H,008H,03FH,0F4H,010H 000H,078H,080H,044H,020H,006H,000H,041H,005H,021H,002H,008H,03FH,0F5H,093H,0FEH DB 097H,0FCH,044H,020H,004H,020H,0FFH,085H,021H,004H,004H,000H,004H,013H,0FEH,097H DB 0FCH,044H,020H,004H,020H,0FFH,085H,021H,004H,004H,000H,005H,0FCH,040H,090H,020H DB 07FH,0E3H,0FFH,0F1H,041H,004H,0BFH,008H,043H,08FH,0C4H,07CH,040H,090H,020H,07FH DB 0E3H,0FFH,0F1H,041H,004H,0BFH,008H,043H,08FH,0C5H,093H,0FCH,018H,020H,044H,020H DB DB 004H,002H,07FH,004H,0A8H,090H,041H,008H,044H,013H,0FCH,018H,020H,044H,020H,004H DB 002H,07FH,004H,0A8H,090H,041H,008H,045H,092H,094H,037H,0FCH,044H,020H,004H,004H 041H,004H,0A9H,000H,080H,008H,044H,012H,094H,037H,0FCH,044H,020H,004H,004H,041H DB DB 004H,0A9H,000H,080H,008H,045H,092H,094H,0D2H,020H,07FH,0E0H,004H,000H,07FH,006H DB 0AAH,000H,090H,00FH,0C4H,012H,094H,0D2H,020H,07FH,0E0H,004H,000H,07FH,006H,0AAH 000H,090H,00FH,0C5H,09AH,094H,011H,020H,044H,000H,004H,000H,041H,005H,024H,001H DB DB 008H,008H,044H,01AH,094H,011H,020H,044H,000H,004H,000H,041H,005H,024H,001H,008H 008H,045H,0E2H,094H,010H,020H,004H,008H,004H,000H,041H,004H,022H,002H,004H,000H DB DB 004H,062H,094H,010H,020H,004H,008H,004H,000H,041H,004H,022H,002H,004H,000H,005H 082H,094H,010H,0A0H,004H,008H,014H,000H,041H,004H,039H,0C7H,0FEH,000H,014H,002H DB DB 094H,010H,0A0H,004H,008H,014H,000H,041H,004H,039H,0C7H,0FEH,000H,015H,082H,00CH DB 010H,040H,003H,0F8H,008H,000H,043H,004H,020H,082H,002H,000H,008H,002H,00CH,010H DB 040H,003H,0F8H,008H,000H,043H,004H,020H,082H,002H,000H,009H,080H,000H,000H,000H DB H000, H000, H000, H000, H000, H000, H000, H100, H000, DB DB DB DB 010H,000H,001H,000H,010H,000H,004H,000H,020H,010H,080H,004H,000H,000H,040H,010H DB 000H,001H,000H,010H,000H,001H,081H,024H,010H,080H,004H,000H,0FFH,0E0H,010H,047H

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